

# Notice of Allowability

Application No.

09/852,923

Examiner

Nathan Curs

Applicant(s)

KIM ET AL.

Art Unit

2633

## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 30 August 2004.
2. ☒ The allowed claim(s) is/are 1-6 and 8-16.
3. ☒ The drawings filed on 10 May 2001 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

  
**M. R. SEDIGHIAN**  
**PRIMARY EXAMINER**

### EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Steve Cha on 1 February 2005.

The corrections in the following amended claims should be made to the applicant's claims of 30 August 2004 to overcome remaining grammar and 112-2<sup>nd</sup> paragraph related problems:

1. (Currently Amended) A bit rate transducer in an optical transmission system, comprising: a demultiplexer for demultiplexing optical signals into different wavelength channels; a plurality of bit rate receivers, each bit rate receiver coupled to [the] an output of said ~~demultiplexing means~~ demultiplexer for converting [said] a demultiplexed optical [signals] signal into [the] a corresponding electrical [signals] signal and for generating a bit-rate error signal, said bit rate receiver having a sensing means for generating a temperature reference signal, a detecting section coupled to ~~the output~~ outputs of said demultiplexer for generating ~~a signal~~ signals indicative of the bit [rate] rates of the optical signals outputted [therefrom] from said demultiplexer; and, a controller for comparing [the] each bit rate detected by said detecting section with a predetermined data to generate a control signal that is used to adjust the bit rate of [said] one of the bit rate [receiver] receivers, wherein said controller compares [the] each bit

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rate detected by said detecting section in response to [said] one of the bit-rate error [signal] signals and [said] one of the temperature reference [signal] signals.

2. (Currently Amended) The bit rate transducer of claim 1, further comprising a switch for outputting said [converted] electric [signals] signal from [the] a respective [said] bit rate receiver to a respective bit rate transmitter.

3. (Currently Amended) The bit rate transducer of claim 1, further [comprises] comprising a parallel-to-serial converter for converting [said] the bit-rate error [signal] signals generated from [the] said plurality of [said] bit rate receivers into serial signals, and a serial-to-parallel converter for supplying the serial signals outputted from said parallel-to-serial converter to said controller as parallel signals.

4. (Currently Amended) The bit rate transducer of claim 1, further [comprises] comprising a first analog-to-digital converter for supplying ~~the signal indicative of temperature of said~~ each temperature reference signal of a bit rate receiver to said controller as a digital [signals] signal.

5. (Currently Amended) The bit rate transducer of claim 1, further comprises a second analog-to-digital converter for supplying the signal indicative of bit rate detected by said detection section to said controller as digital signals.

6. (Currently Amended) The bit rate transducer of claim 1, wherein said predetermined data comprises a list of reference ~~temperature with the~~ temperatures with corresponding reference bit rates.

7. (Canceled)

8. (Currently Amended) The bit rate transducer of claim 1, wherein said controller generates [said] a control signal based on [said] one of the temperature reference [signal] signals and [said] one of the bit-rate error [signal] signals received thereon with said predetermined data.

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9. (Currently Amended) The bit rate transducer of claim 1, further comprising a means for generating said bit-rate error signal, wherein said bit-rate error signal is generated based on a difference between the [detected] bit rate of the electrical signal converted by said bit rate receiver and a pre-set bit rate.

10. (Currently Amended) A bit rate transducer in an optical transmission system, comprising: a plurality of bit rate transmitters for converting incoming electrical signals into [the] corresponding optical signals and for generating [a] bit-rate error [signal] signals, [said] each bit rate transmitter having a sensing means for generating a temperature reference signal; a multiplexer for multiplexing [said] the converted optical signals outputted from [the] said plurality of [said] bit rate transmitters; a detecting section coupled to ~~the output~~ outputs of said multiplexer for generating ~~a signal~~ signals indicative of the bit [rate] rates of the [electrical] optical signals outputted therefrom; and, a controller for comparing [the] each bit rate detected by said detecting section with a predetermined data to generate a control signal that is used to adjust the bit rate of [said] one of the bit rate [transmitter] transmitters, wherein said controller generates [said] a control signal based on [said] one of the temperature reference [signal] signals and [said] one of the bit-rate error [signal] signals received thereon with said predetermined data.

11. (Previously Presented) The bit rate transducer of claim 10, further comprising a switch for providing said incoming electrical signals to the plurality of said bit rate transmitters.

12. (Currently Amended) The bit rate transducer of claim 10, further [comprises] comprising a parallel-to-serial converter for converting [said] the bit-rate error [signal] signals generated from [the] said plurality of [said] bit rate transmitters into serial signals, and a serial-to-parallel converter for supplying the serial signals outputted from said parallel-to-serial converter to said controller as parallel signals.

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13. (Currently Amended) The bit rate transducer of claim 10, further [comprises] comprising a first analog-to-digital converter for supplying ~~the signal indicative of temperature of~~ said each temperature reference signal of a bit rate transmitter to said controller as a digital [signals] signal.

14. (Currently Amended) The bit rate transducer of claim 10, further [comprises] comprising a second analog-to-digital converter for supplying [the] each signal indicative of a bit rate detected by said detection section to said controller as a digital [signals] signal.

15. (Currently Amended) The bit rate transducer of claim 10, wherein said predetermined data comprises a list of reference [temperature] temperatures with [the] corresponding reference bit rates.

16. (Currently Amended) The bit rate transducer of claim 10, wherein said controller compares [the] each bit rate detected by said detecting section in response to [said] one of the bit-rate error [signal] signals and [said] one of the temperature reference [signal] signals.

17. (Canceled)

### **Conclusion**

2. Any inquiry concerning this communication from the examiner should be directed to N. Curs whose telephone number is (571) 272-3028. The examiner can normally be reached M-F (from 9 AM to 5 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jason Chan, can be reached at (571) 272-3022. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571) 272-2600.

  
**M. R. SEDIGHIAN**  
**PRIMARY EXAMINER**